

FIG. 1

SHEET 2/13

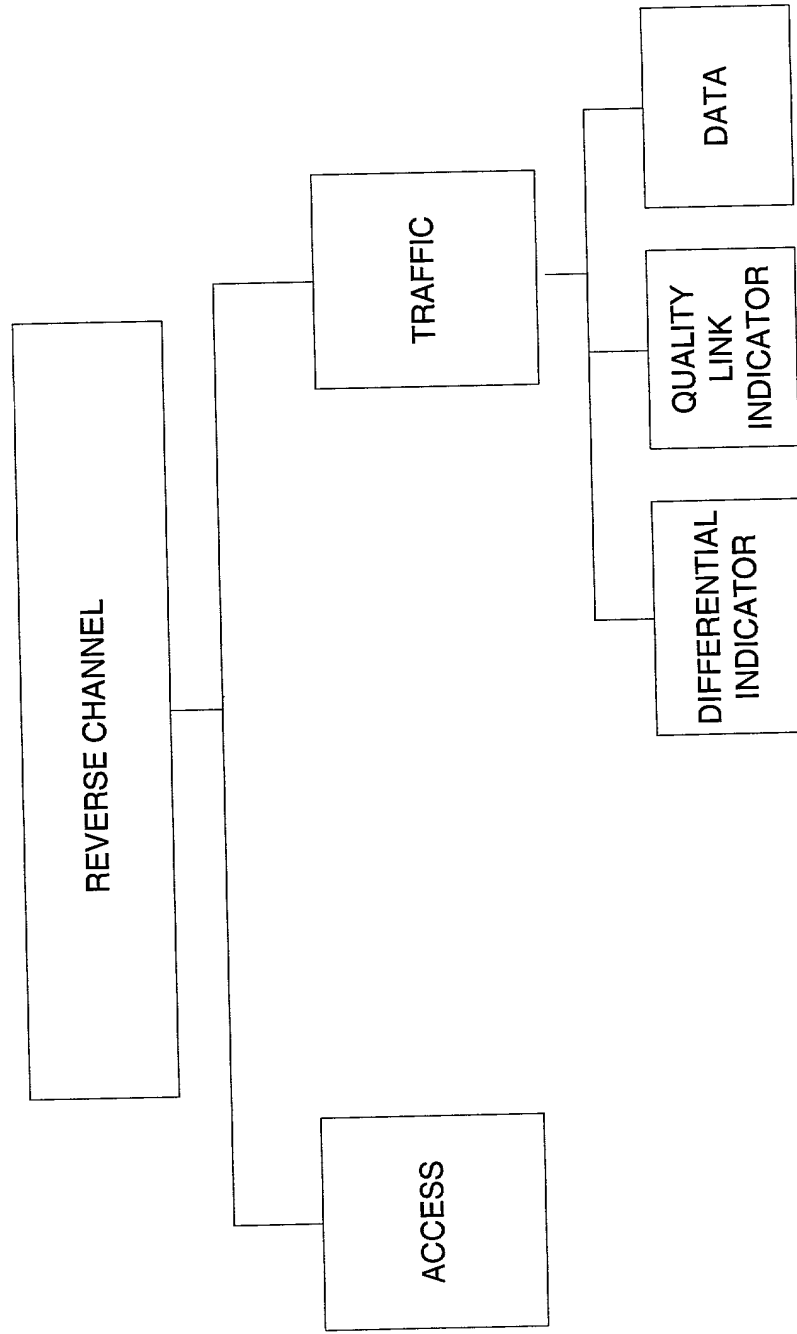


FIG. 2

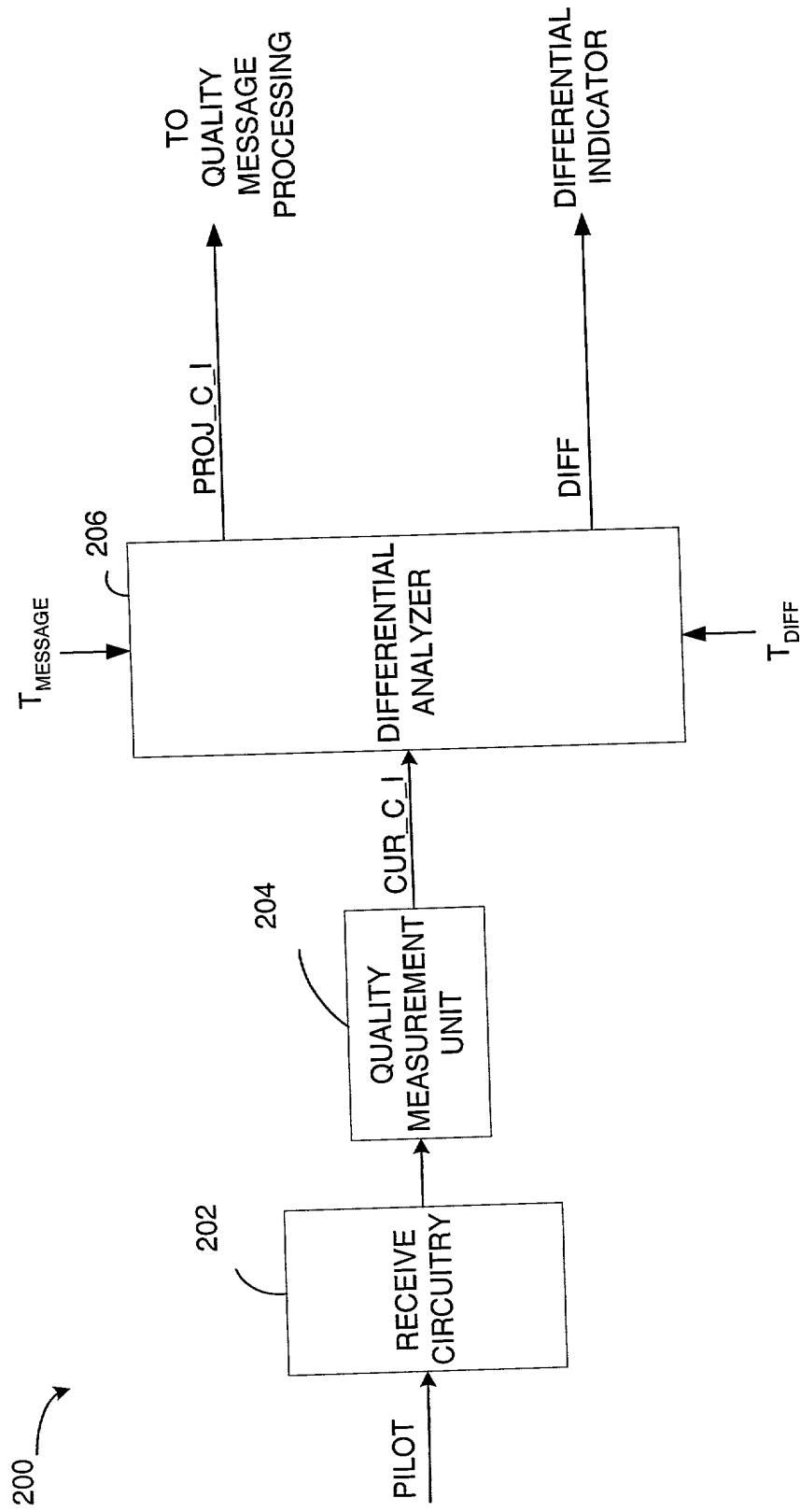


FIG. 3A

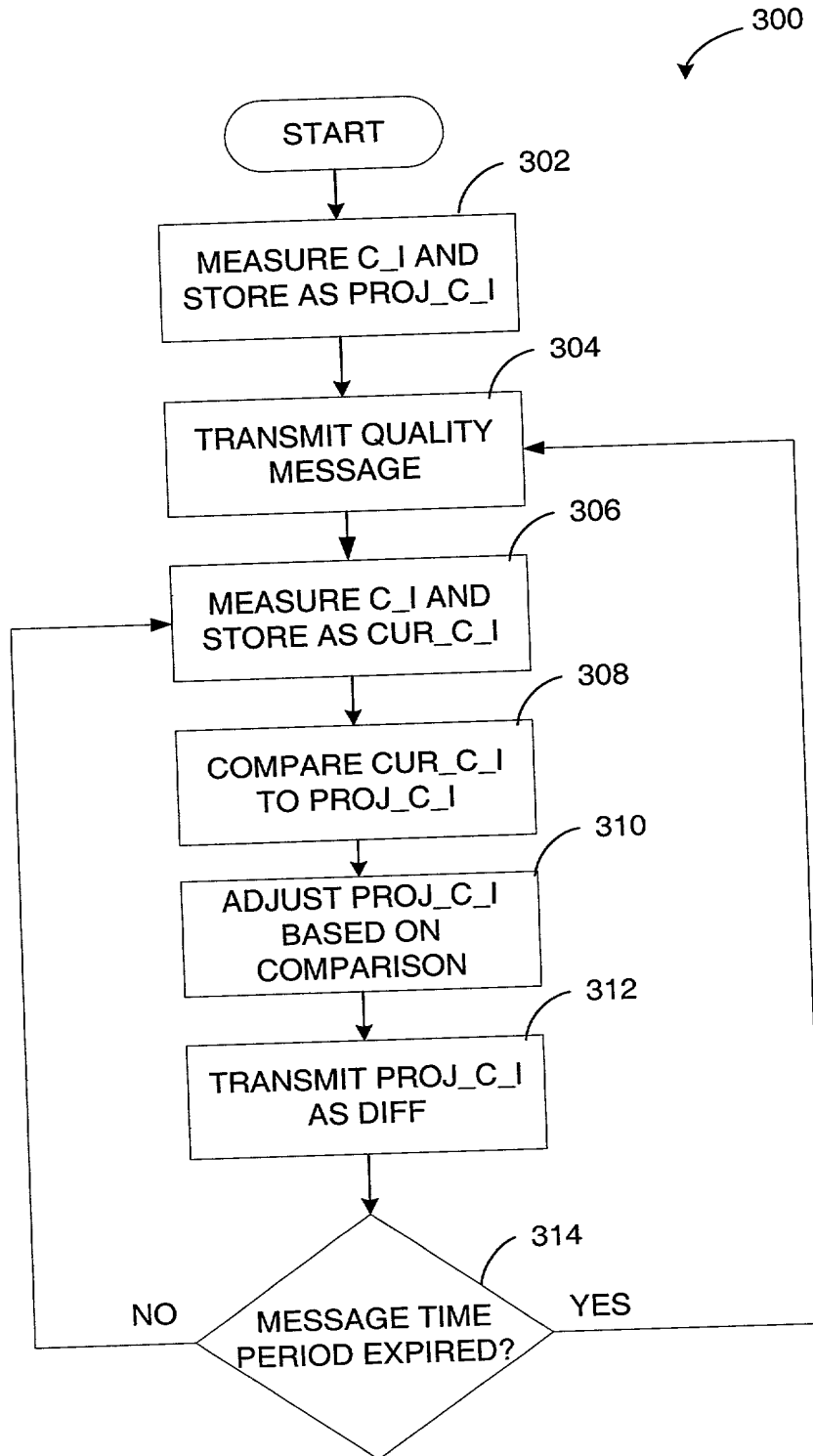


FIG. 3B

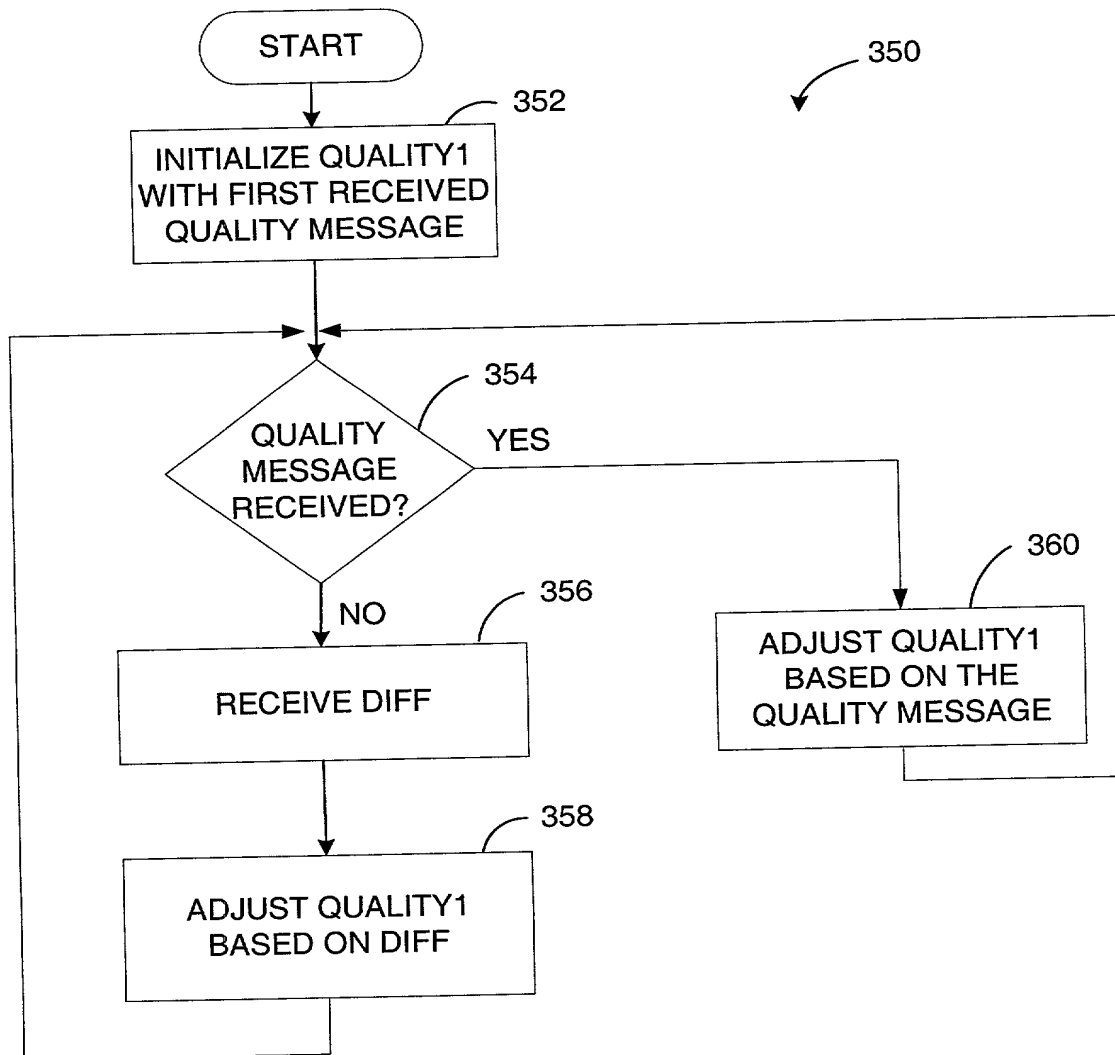


FIG. 3C

SHEET 6/13

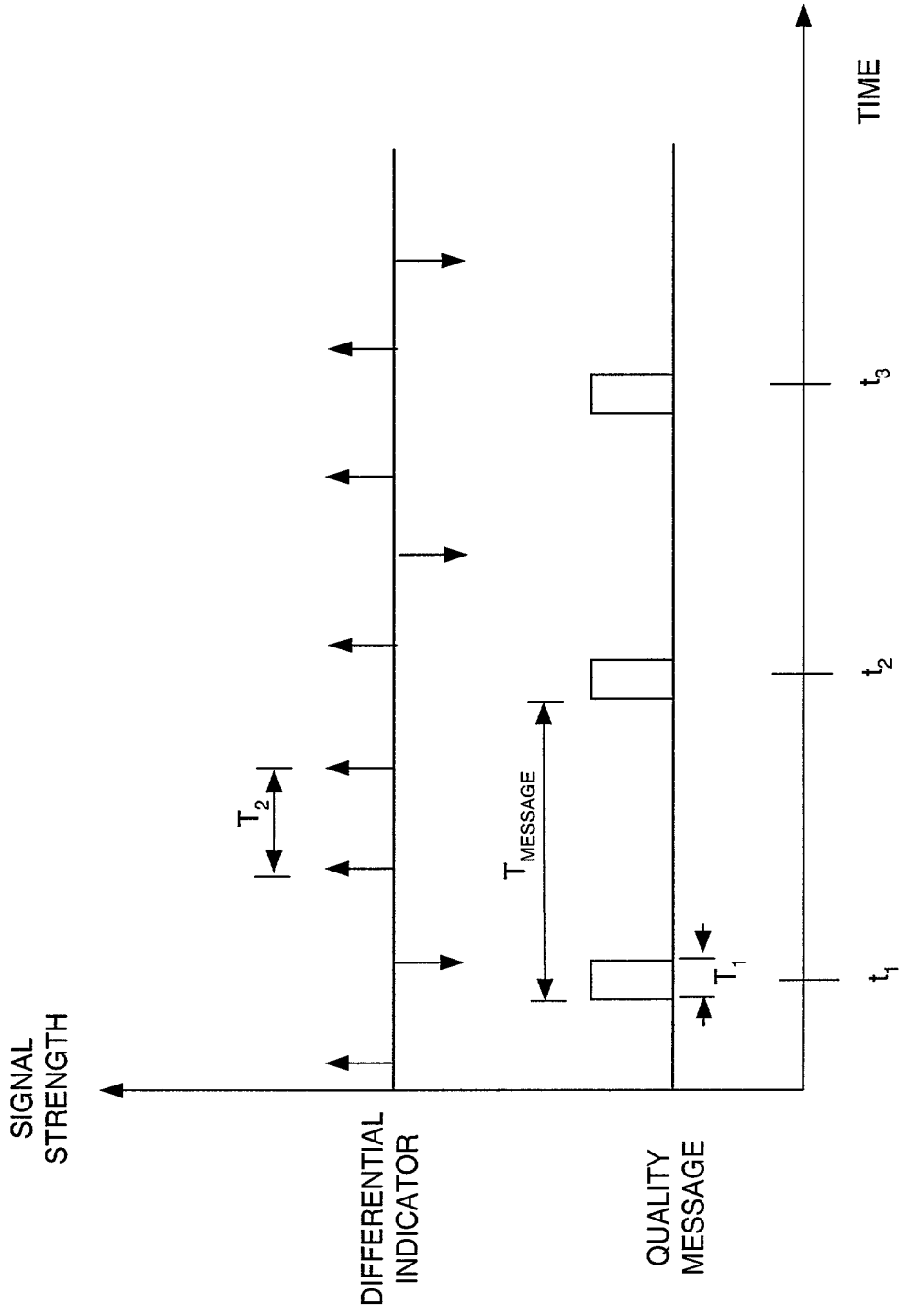


FIG. 3D

400

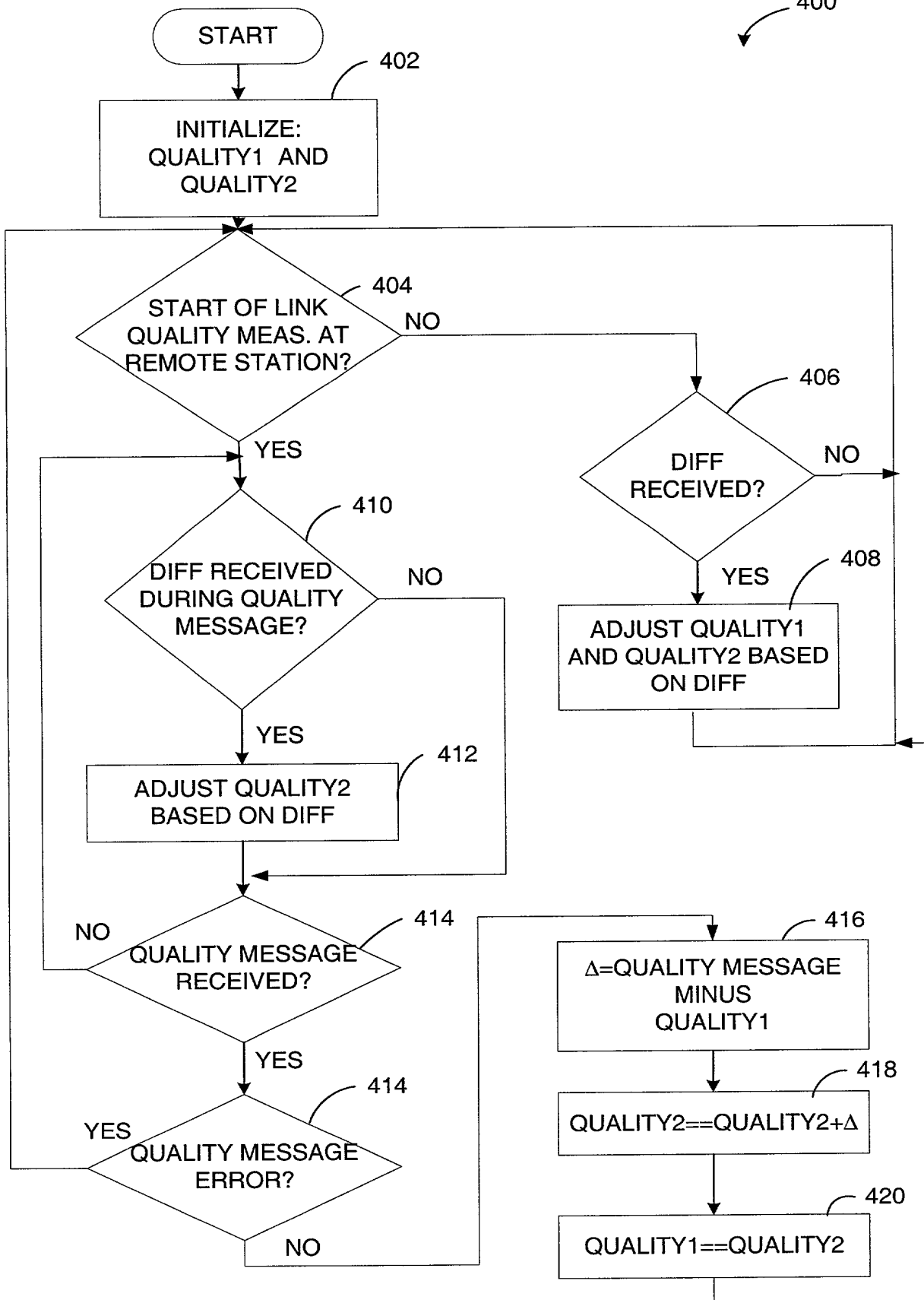


FIG. 4A

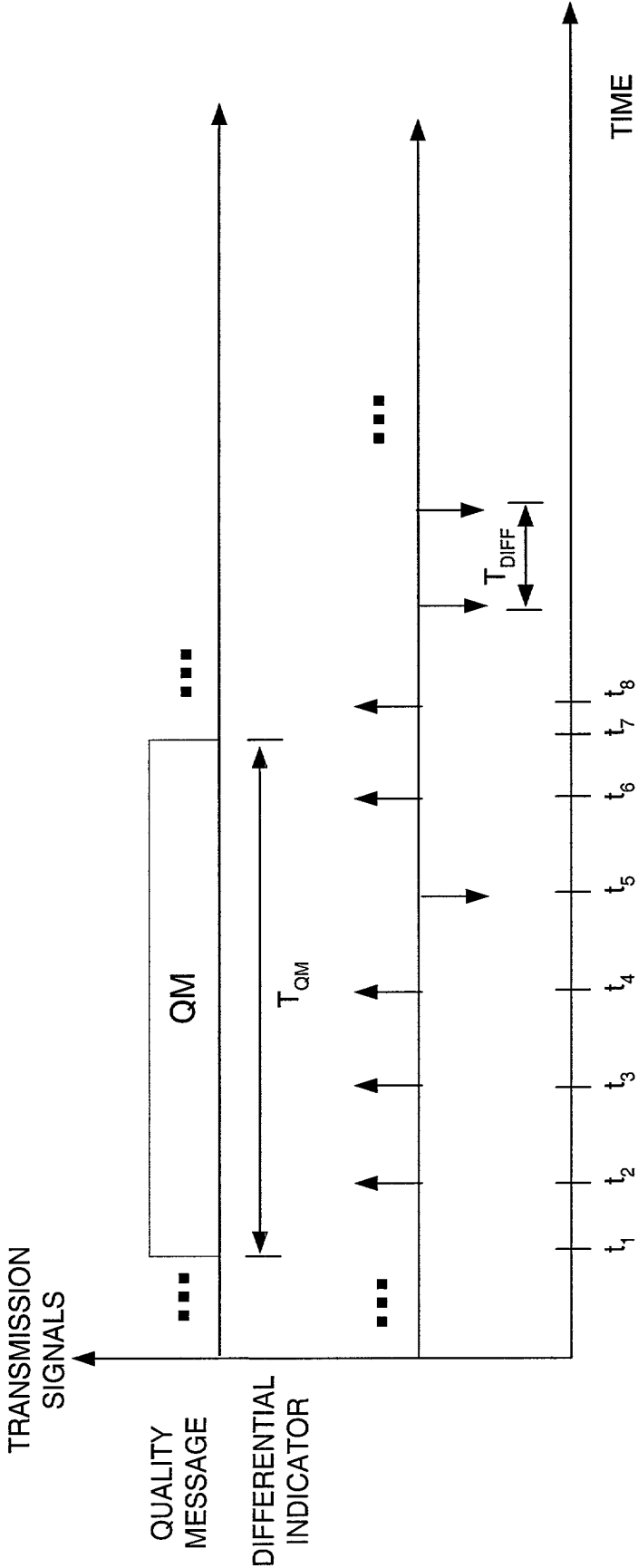
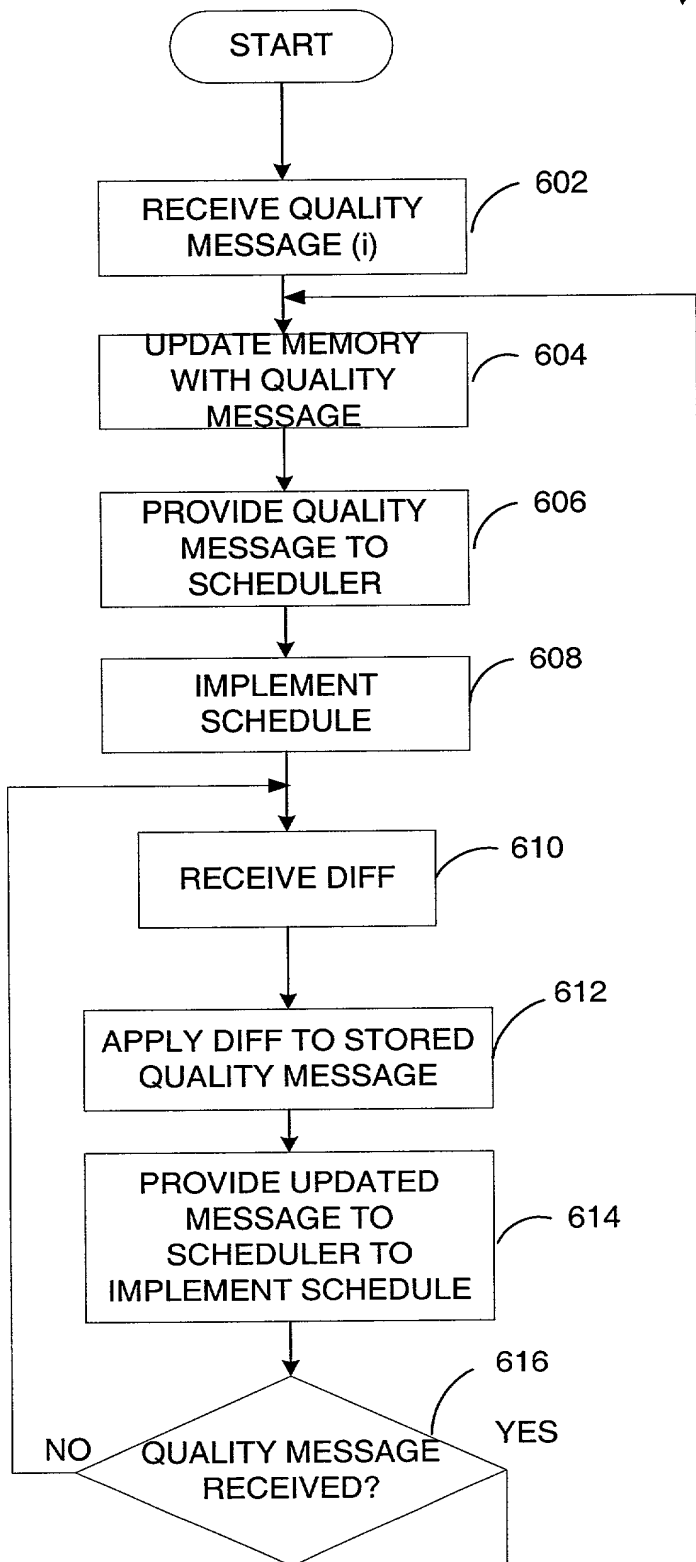


FIG. 4B

Quality Variable	t_1	t_2	t_3	t_4	t_5	t_6	t_7	t_8
QUALITY1	A	A	A	A	A	A	A	B
DIFF	--	+1	+1	+1	(-1)	+1	--	+1
QUALITY2	A	A+1	A+2	A+3	A+2	A+3	A+3	A+4+Δ

FIG. 4C

**FIG. 5**

SHEET 10/13

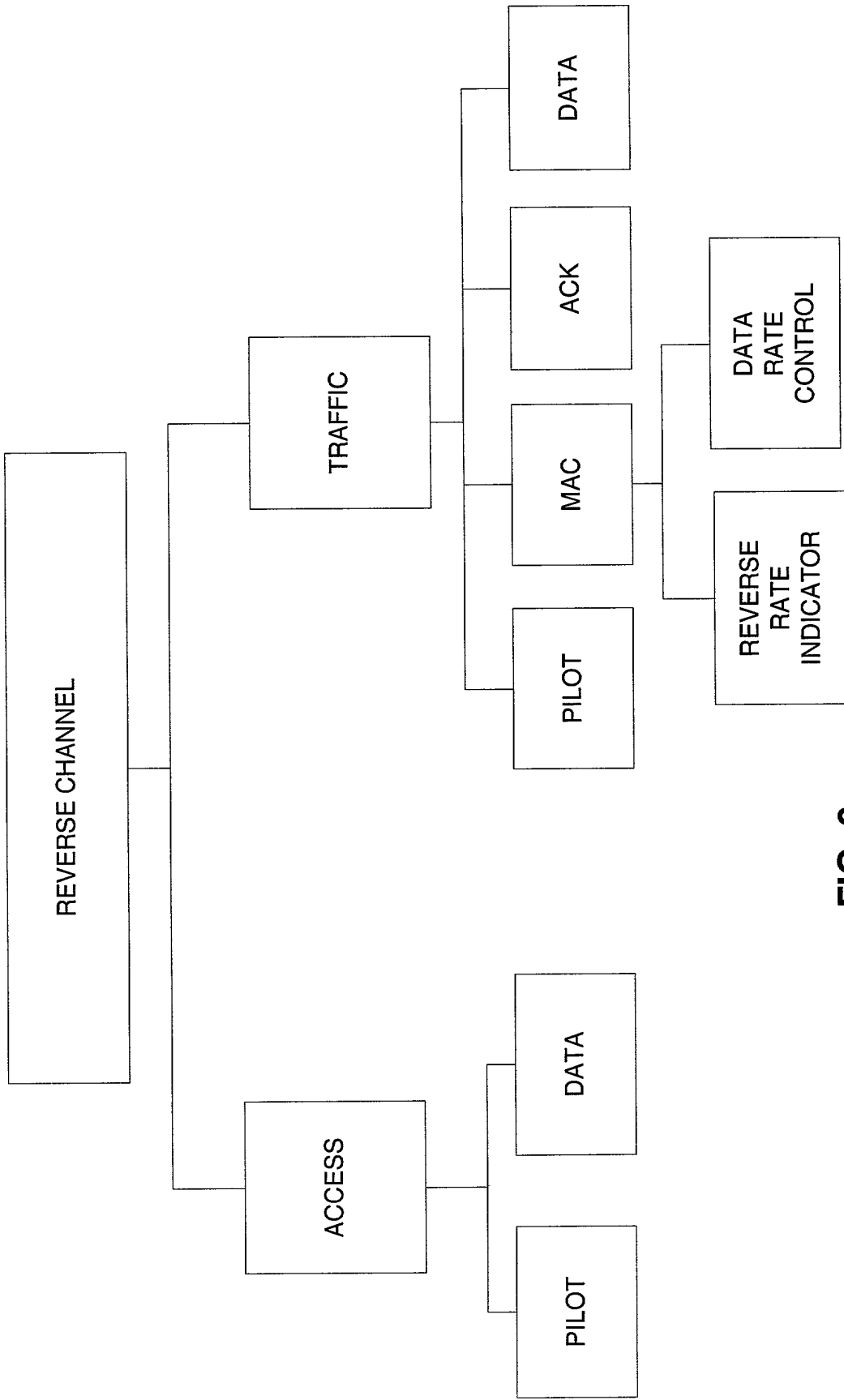


FIG. 6

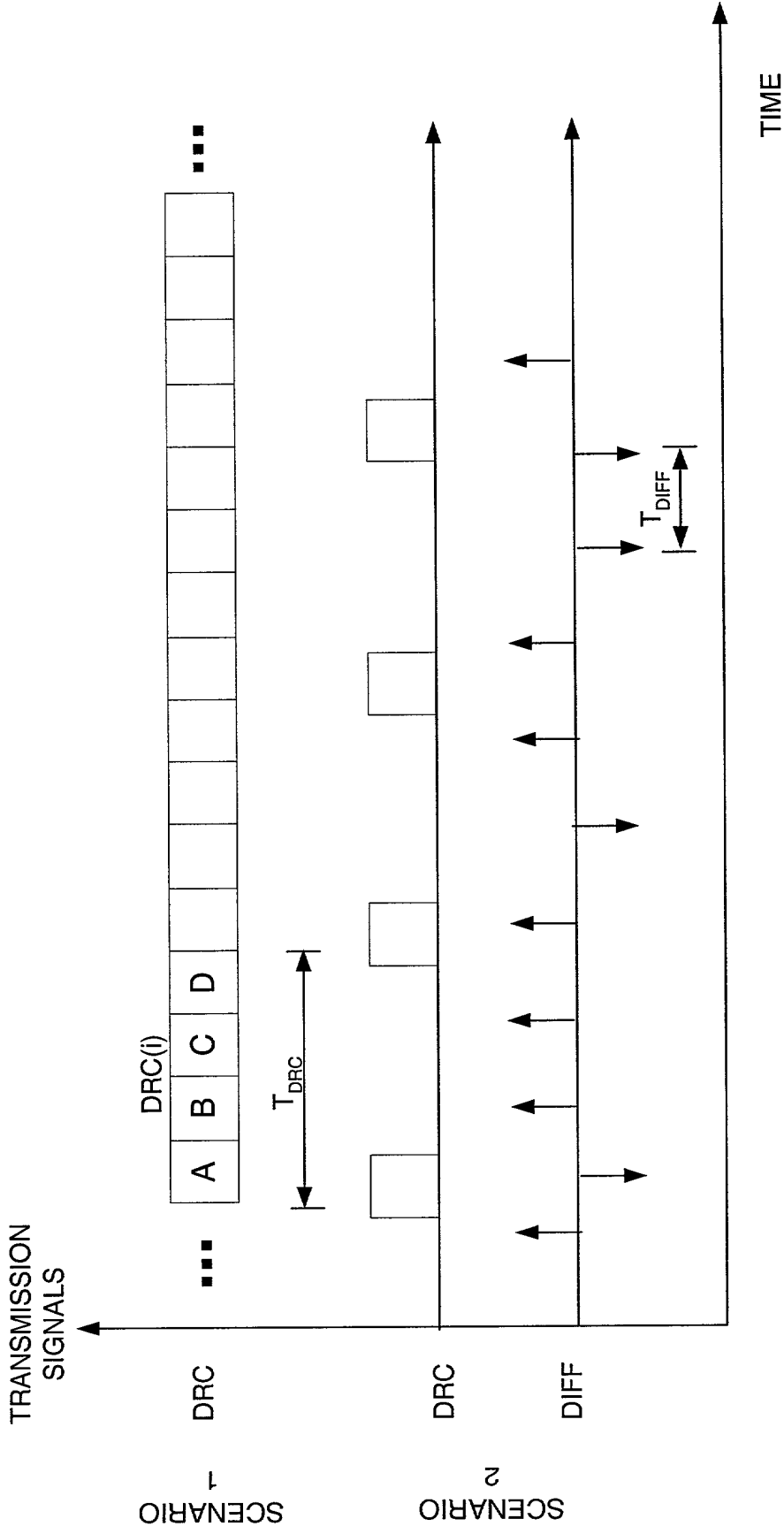


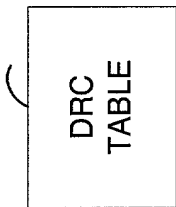
FIG. 7

DRC MESSAGE	RATE (kbps)	PACKET LENGTH (slots)
0x0	null rate	N/A
0x1	38.4	16
0x2	76.8	8
0x3	153.6	4
0x4	307.2	2
0x5	307.2	4
0x6	614.4	1
0x7	614.4	2
0x8	921.6	2
0x9	1228.8	1
0xa	1228.8	2
0xb	1843.2	1
0xc	2457.6	1
0xd	INVALID	N/A
0xe	INVALID	N/A
0xf	INVALID	N/A

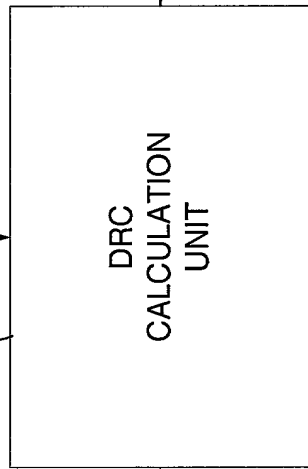
FIG. 8

SHEET 13/13

500



504



RECEIVED
SIGNAL

DIFFERENTIAL
ANALYZER

T_{DRC}

506

TO
DRC
MESSAGE
PROCESSING

DIFFERENTIAL
INDICATOR

T_{DIFF}

FIG. 9